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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/507,158	08/19/2004	Dominique Gagnot	PF020013	8977

7590
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12/13/2007

EXAMINER

AMADIZ, RODNEY

ART UNIT	PAPER NUMBER
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2629

MAIL DATE	DELIVERY MODE
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PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No. 10/507,158	Applicant(s) GAGNOT, DOMINIQUE	
	Examiner Rodney Amadiz	Art Unit 2629	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 19 August 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-12 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-12 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 19 August 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date <u>8/19/04</u> | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Objections

1. Claim 9 objected to because of the following informalities: Please delete "(1)" in line 2. Also, please delete "-" in page 15, line 4. Also, line 12 refers to "supply and drive means according to any one of the preceding claims", please amend to "a drive circuit according to any one of the preceding claims" to be consistent with the preliminary amendment of claim 1. Appropriate correction is required.
2. Claims 2-8 are objected to because of the following informalities: It is suggested to delete the phrase "Supply and drive means" at the beginning of each claim and replace it with "The drive circuit according to claim" to be consistent with the preliminary amendment to claim 1. Appropriate correction is required.

Claim Rejections - 35 USC § 112

3. Claim 2 recites the limitation "the said capacitive energy" in lines 3. There is insufficient antecedent basis for this limitation in the claim. Furthermore, the entire claim lacks antecedent basis because it reverts back to the limitation "inductance for recovering and re-injecting" which was deleted from Claim 1.
4. Claim 9 recites the limitation "the memory effect" in Pg. 15, line 5. There is insufficient antecedent basis for this limitation in the claim.
5. Claim 10 recites the limitation "the high terminal" and "the low terminal" in lines 2-3. There is insufficient antecedent basis for this limitation in the claim. Claims 11 and 12 depend from Claim 10 and are therefore also rejected.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

6. Claims 1 and 3-9 are rejected under 35 U.S.C. 102(b) as being anticipated by Tezucar et al. (U.S. Patent 4,533,913—hereinafter “Tezucar”).

As to **Claim 1**, Tezucar teaches a drive circuit for a plasma panel comprising: two parallel plates leaving between them a space containing a discharge gas (***Fig. 1, 20 and 30 and Col. 1, lines 60-67***); first and at least a second array of sustain electrodes (***100A and 100B***) associated in pairs of an electrode of the first array and of an adjacent electrode of the second array so that the electrodes of the same pair define between them corresponding respective luminous discharge regions in the space between the plates (***Col. 3, line 49—Col. 4, line 6***); at least one dielectric layer (***130***) covering at least one of the said arrays of sustain electrodes at least one transformer each comprising at least one primary circuit and at least one secondary circuits coupled to the said primary circuit such that said at least one transformer provides pulses capable of generating luminous discharges in said discharge regions (***Fig. 7, 240, 280, 340, 380 and 430***).

As to **Claim 3**, Tezucar teaches write or erase means designed to apply a write voltage pulse or erase voltage pulse to the secondary circuit supplying the said pair of electrodes (***See Fig. 5***).

As to **Claim 4**, Tezucar teaches a plurality of H transformers (***Fig. 7, 240, 280, 340, 380 and 430***) and wherein the write or erase means comprise a combination of L line drivers, each driver being intended to apply a write voltage pulse or erase voltage pulse to a plurality of H secondary circuits (***See Fig. 5***).

As to **Claim 5**, Tezucar teaches a write or erase bias pulse generator and means for connecting this generator to the primary circuits of the transformers (***See Figs. 3 and 5 and note that it is inherent that the "Write/Erase Pulses" must be generated by some means***).

As to **Claim 6**, Tezucar teaches that the write or erase means are designed so that each write voltage pulse or erase voltage pulse applied to any one secondary circuit is applied while a write or erase bias pulse is applied to the primary circuit magnetically coupled to the said secondary circuit (***See Fig. 5***).

As to **Claim 7**, Tezucar teaches that said write or erase means are designed to apply a plurality of write voltage pulses or erase voltage pulses to various secondary circuits coupled to the same primary circuit during a bias pulse applied to the said primary circuit (***See Figs. 5 and 7***).

As to **Claim 8**, Tezucar teaches means for triggering a train of write or erase oscillations in a primary circuit and means for triggering each new train of bias oscillations of another primary circuit immediately at the end of the first bias pulse of the previous train of oscillations (***See Figs. 5 and 7***).

As to **Claim 9/1-8**, Tezucar teaches an image display system, comprising an AC plasma panel (1) comprising: two parallel plates leaving between them a space

containing a discharge gas (**Fig. 1, 20 and 30 and Col. 1, lines 60-67**); a first and at least a second array of sustain electrodes associated in pairs (**100A and 100B**) of an electrode of the first array and of an adjacent electrode of the second array so that the electrodes of the same pair define between them a succession of luminous discharge regions in the space between the plates (**Col. 3, line 49—Col. 4, line 6**); a dielectric layer (**130**) covering at least one of the said sustain arrays in order to provide the memory effect, wherein it comprises supply and drive means according to any one of the preceding claims, which are associated with the said panel so as to be able to supply and drive it (**Fig. 3, 180**).

Conclusion

7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Kishi et al.

U.S. Patent 6,686,912

Iwasa et al.

U.S. Patent 6,937,213

Inquiries

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Rodney Amadiz whose telephone number is (571) 272-7762. The examiner can normally be reached on M-F 8:30-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sumati Lefkowitz can be reached on (571) 272-3638. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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12/5/07
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